

in Re Application of

Serial No.: 10/757,193

**For: Method For Manufacturing Resist
Pattern And Method For
Manufacturing Semiconductor
Device**

Examiner: Unknown

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATUS INQUIRY

Gentlemen:


Please advise as to the status of the above-identified patent application.

More than two (2) years have passed since Applicants filed this application.

Accordingly, the Applicants respectfully requests the current status of the above-identified patent application.

Respectfully submitted,

Dated: October 2, 2006


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